

HydraNFC_LogicAnalyzer

LogicAnalyzer Chan	HydraNFC Pin	TRF7970A Pin	HydraBus Mode SPI With SS
0	PC3	IO7_MOSI	SPI2_MOSI Master out
1	PC2	IO6_MISO	SPI2_MISO Master or RX DM1 in
2	PB10	DATA_CLK	SPI2_CLK Master out
3	PC1	IO4_CS	SPI2_CS Master out
4	PA7	MOD	SPI1_MOSI Slave (sniffer) in
5	PA5	SYS_CLK (Out)	SPI1_CLK Slave (sniffer) in
6	PA1	IRQ	IRQ Input in
7	PC4	IO5	TX SDM or RX DM1 CLK in
8	PC5	IO3	TX SDM out
9	PB1	ASK_OOK	TX SDM or Direct Mode 0 out
10	PB11	EN (Chip En)	+3V3 out
11	PC0	IO2	+3V3 or TX SDM (Data En) out
-	PA2	IO1	+3V3 out
-	PA3	IO0	GND out