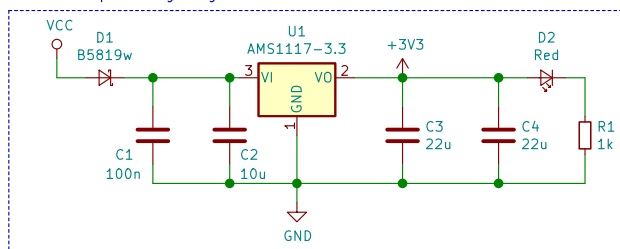
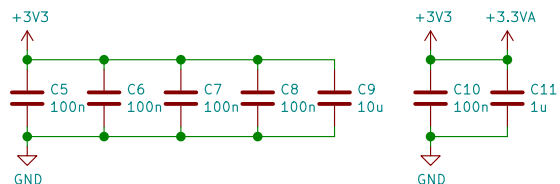


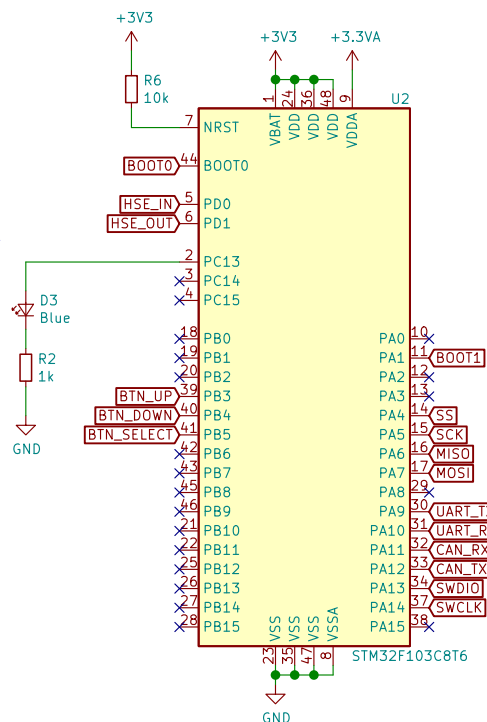
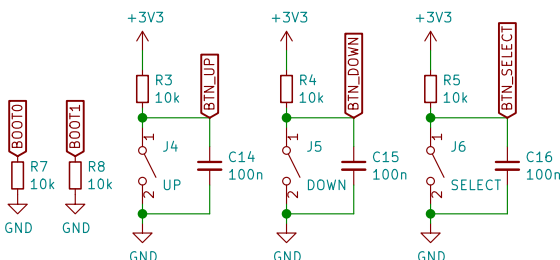
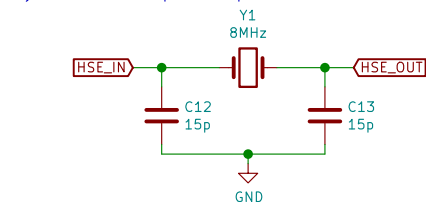
Input Voltage Regulator



AN2586 section 1.2

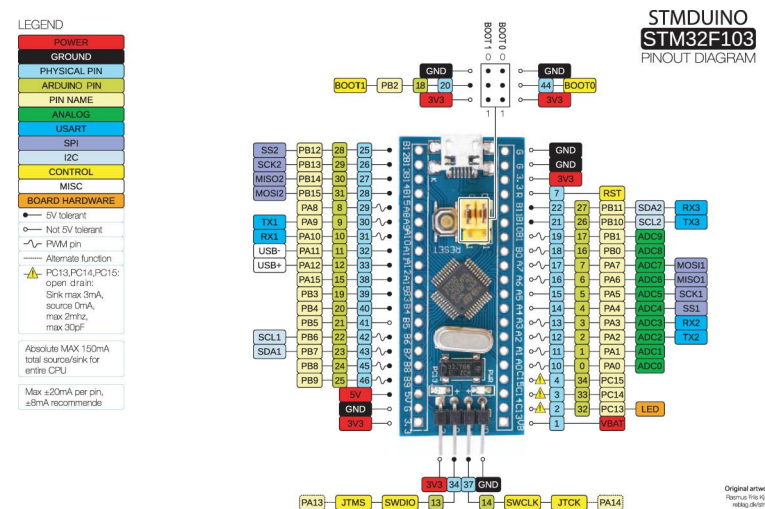
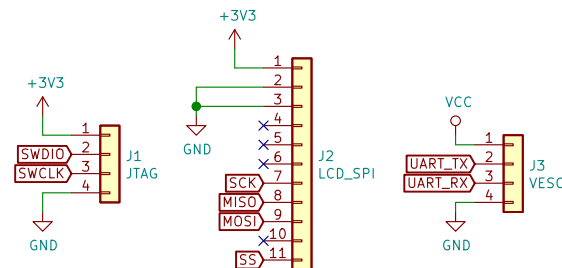


CL = CL1 x CL2 / (CL1 + CL2) + Cstray where: Cstray is the pin capacitance and board or trace PCB-related capacitance. Typically, it is between 2 pF and 7 pF. Maximum value of CL1 and CL2 is 15 pF



LEGEND

| |
|--|
| POWER |
| GROUND |
| PHYSICAL PIN |
| ARDUINO PIN |
| PIN NAME |
| ANALOG |
| USART |
| SPI |
| I2C |
| CONTROL |
| MISC |
| BOARD HARDWARE |
| 5V tolerant |
| Not 5V tolerant |
| PWM pin |
| Alternate function |
| PC13/PC14/PC15: open drain: Sink max 3mA, source 0mA, max 2mHz, max 30pF |
| Absolute MAX 150mA total source/sink for entire CPU |
| Max ±20mA per pin, ±9mA recommende |



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