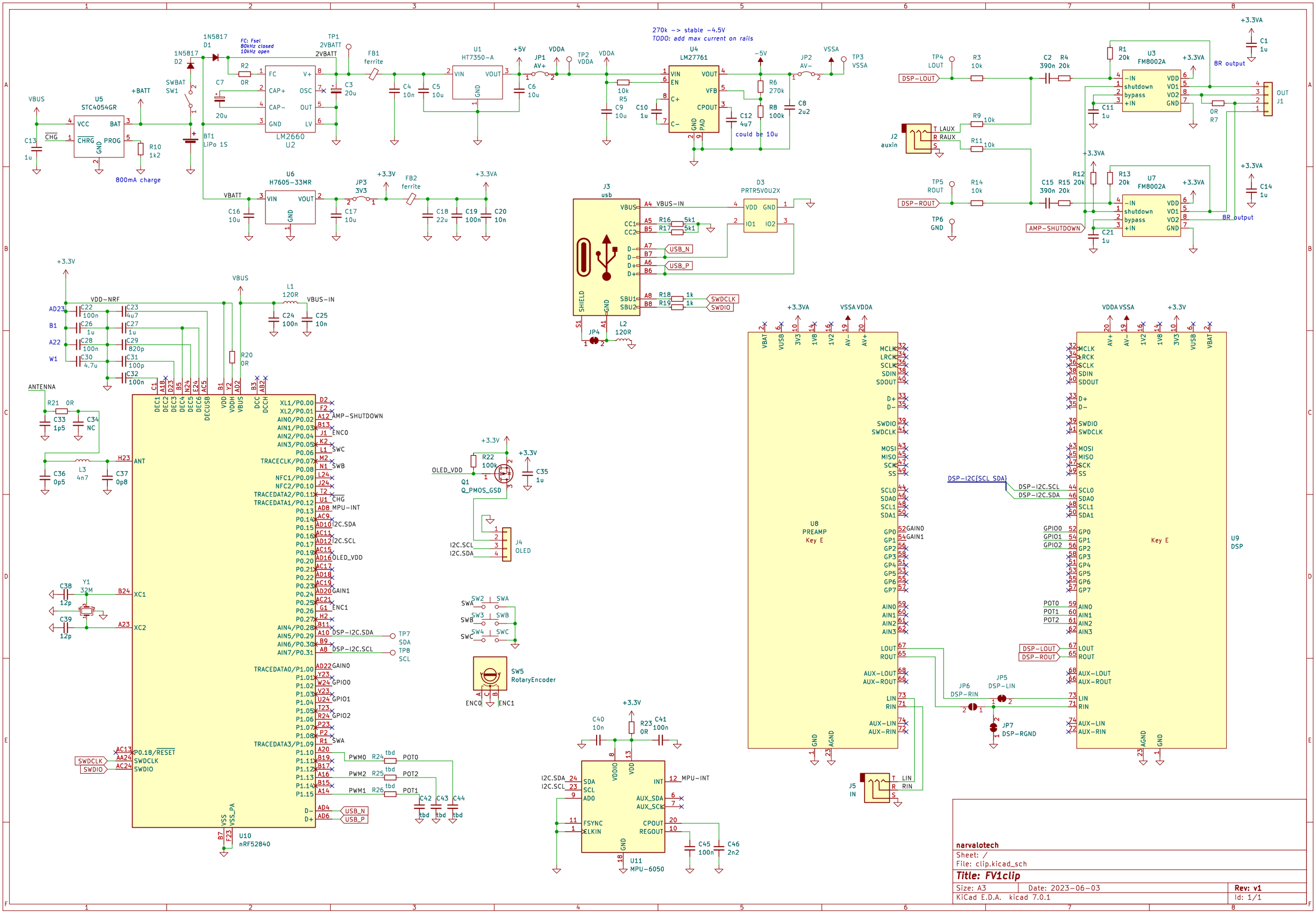
1N5817
D1
1N5817
D2
FC: Fsel
80kHz closed
10kHz open
TP1
2VBATT
2VBATT
FB1
ferrite
R2
0R
C7
20u
C3
20u
LM2660
U2
C4
10n
C5
10u
U1
HT7350-A
+5V
JP1
AV+
VDDA
TP2
VDDA
270k -> stable -4.5V
TODO: add max current on rails
U4
LM27761
-5V
JP2
AV-
VSSA
TP3
VSSA
R6
270k
R8
100k
C8
2u2
C12
4u7
could be 10u
C10
1u
R5
10k
C9
10u
C6
10u
VBUS
U5
STC4054GR
+BATT
SWBAT
SW1
BT1
LiPo 1S
C13
1u
R10
1k2
800mA charge
U6
H7605-33MR
VBATT
JP3
3V3
+3.3V
FB2
ferrite
+3.3VA
C16
10u
C17
10u
C18
22u
C19
100n
C20
10n
J3
usb
VBUS-IN
USB_N
USB_P
SWDCLK
SWDIO
D3
PRTR5V0U2X
TP4
LOUT
DSP-LOUT
R3
10k
C2
390n
R4
20k
R1
20k
U3
FM8002A
+3.3VA
8R output
C1
1u
C11
1u
R9
10k
R11
10k
J2
auxin
LAUX
RAUX
TP5
ROUT
DSP-ROUT
R14
10k
C15
390n
R15
20k
R12
20k
R13
20k
U7
FM8002A
C21
1u
TP6
GND
AMP-SHUTDOWN
OUT
J1
R7
0R
C14
1u
+3.3V
L1
120R
VBUS-IN
C24
100n
C25
10n
VDD-NRF
C22
100n
C23
4u7
C26
1u
C27
1u
C28
100n
C29
820p
C30
4.7u
C31
100p
C32
100n
R20
0R
ANTENNA
R21
0R
C33
1p5
C34
NC
L3
4n7
C36
0p5
C37
0p8
H23
ANT
Y1
32M
C38
12p
C39
12p
XC1
XC2
SWDCLK
SWDIO
P0.18/RESET
U10
nRF52840
XL1/P0.00
XL2/P0.01
AIN0/P0.02
AIN1/P0.03
AIN2/P0.04
AIN3/P0.05
P0.06
TRACECLK/P0.07
P0.08
NFC1/P0.09
NFC2/P0.10
TRACEDATA2/P0.11
TRACEDATA1/P0.12
P0.13
P0.14
P0.15
P0.16
P0.17
P0.19
P0.20
P0.21
P0.22
P0.23
P0.24
P0.25
P0.26
P0.27
AIN4/P0.28
AIN5/P0.29
AIN6/P0.30
AIN7/P0.31
TRACEDATA0/P1.00
P1.01
P1.02
P1.03
P1.04
P1.05
P1.06
P1.07
P1.08
TRACEDATA3/P1.09
P1.10
P1.11
P1.12
P1.13
P1.14
P1.15
D-
D+
AMP-SHUTDOWN
ENCO
SWC
SWB
MPU-INT
I2C.SDA
I2C.SCL
OLED_VDD
GAIN0
GAIN1
ENC1
DSP-I2C.SDA
DSP-I2C.SCL
TP7
SDA
TP8
SCL
CHG
USB_N
USB_P
PWM0
PWM2
PWM1
POT0
POT2
POT1
R24
R25
R26
tbd
C42
C43
C44
+3.3V
R22
100k
Q1
Q_PMOS_GSD
C35
1u
J4
OLED
SW2
SW3
SW4
SWA
SWB
SWC
SW5
RotaryEncoder
ENC0
ENC1
C40
10n
R23
0R
C41
100n
U11
MPU-6050
C45
100n
C46
2n2
U8
PREAMP
Key E
U9
DSP
Key E
VBAT
VUSB
3V3
1V8
1V2
AV-
AV+
MCLK
LRCK
SCLK
SDIN
SDOUT
D+
D-
SWDIO
SWDCLK
MOSI
MISO
SCK
SS
SCL0
SDA0
SCL1
SDA1
GP0
GP1
GP2
GP3
GP4
GP5
GP6
GP7
GPIO0
GPIO1
GPIO2
POT0
POT1
POT2
AIN0
AIN1
AIN2
AIN3
LOUT
ROUT
AUX-LOUT
AUX-ROUT
LIN
RIN
AUX-LIN
AUX-RIN
JP6
DSP-RIN
JP5
DSP-LIN
JP7
DSP-RGND
J5
IN
T LIN
R RIN
S
GND
AGND
narvalotech
Sheet: /
File: clip.kicad_sch
Title: FV1clip
Size: A3
Date: 2023-06-03
Rev: v1
KiCad E.D.A. kicad 7.0.1
Id: 1/1